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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/813,031
	Filing Date	March 31, 2004
	First Named Inventor	Hiroki GOKO et al.
	Group Art Unit	2825

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		Examiner Name	Suresh Memula		
Total Number of Pages in This Sul	ebmission 17	Attorney Docket Number	030712-36		
	ENCLOSU	RES (check all that apply)			
Fee Transmittal Form Fee Attached Amendment / Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statemed Certified Copy of Priority Document(s) Response to Missing Parts/ Incomplete Application Response to Missing Parts under 37 CFR 1.52 or 1.53	CD, Nu	tion and Power of Attorney ng-related Papers to Convert to a Provisional	□ After Allowance Communication to Group □ Appeal Communication to Board of Appeals and Interferences □ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) □ Proprietary Information □ Status Letter □ Application Data Sheet □ Request for Corrected Filing Receipt with Enclosures □ A self-addressed prepaid postcard for acknowledging receipt □ Other Enclosure(s) (please identify below):		
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Firm or Individual name SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Donald R. Studebaker – Reg. No. 32,815 Nixon Peabody LLP 401 9 th Street, N.W. Suite 900 Washington, D.C. 20004-2128					
Signature					
Date	May 22, 2006				
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
Hiroki Goko)	Group Art Unit: 2121
Serial No. 10/813,031)	Examiner: MEMULA, Suresh
Filed: March 31, 2004)	Confirmation No. 3838
For: APPARATUS AND METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT)	Date: May 22, 2006

AMENDMENT

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed February 21, 2006 please consider Applicant's remarks and amendments in relation to the above identified application as follows.